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STATEMENT UNDER 37 CFR 3.73(b) Applicant/Patent Owner: Richard S. Roy Filed/Issue Date: 04/26/2005 Application No./Patent No.: 6886078 Titled: SIMULTANEOUS ACCESS AND CACHE LOADING IN A HIERARCHICALLY ORGANIZED MEMORY CIRCUIT , a Delaware corporation Synopsys, Inc. (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc. states that it is: the assignee of the entire right, title, and interest in: an assignee of less than the entire right, title, and interest in (The extent (by percentage) of its ownership interest is %); or the assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made) the patent application/patent identified above, by virtue of either: An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel ______, Frame ______, or for which a copy therefore is attached. OR B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows: 1. From: Richard S. Roy To: Virage Logic Corporation The document was recorded in the United States Patent and Trademark Office at Reel 012191 , Frame 0341 , or for which a copy thereof is attached. 2. From: Virage Logic Corporation To: Synopsys, Inc. The document was recorded in the United States Patent and Trademark Office at Reel 025105 Frame 0907 _____, or for which a copy thereof is attached. 3. From: The document was recorded in the United States Patent and Trademark Office at Reel , Frame , or for which a copy thereof is attached, Additional documents in the chain of title are listed on a supplemental sheet(s).

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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

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